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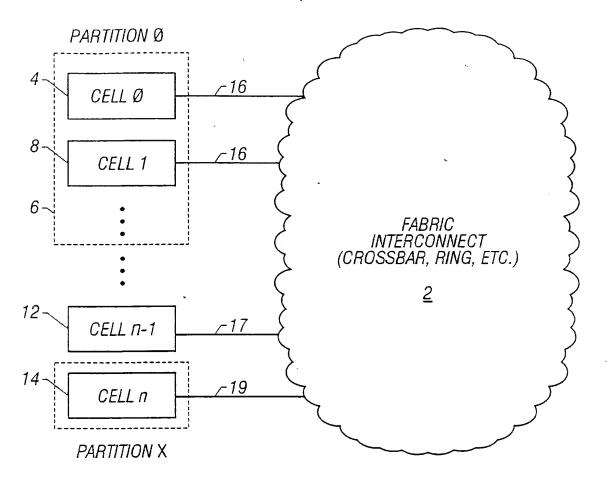


FIG. 1

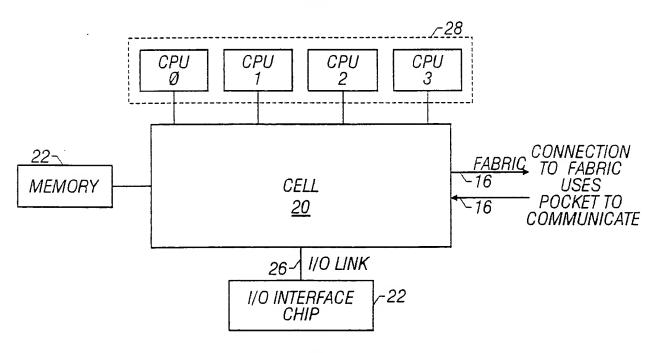


FIG. 2

## METHOD FOR ALLOWING DISTRIBUTED HIGH PERFORMANCE COHERENT MEMORY WITH FULL ERROR CONTAINMENT

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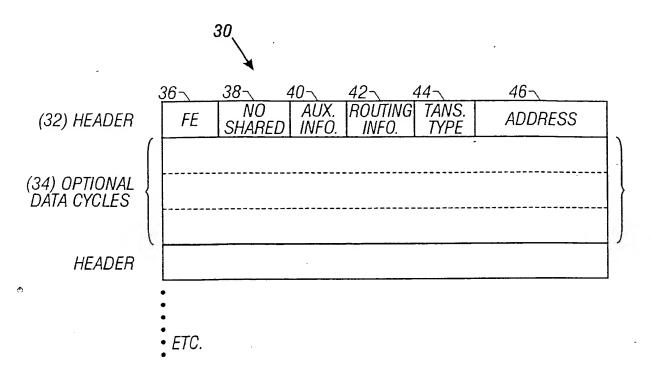


FIG. 3

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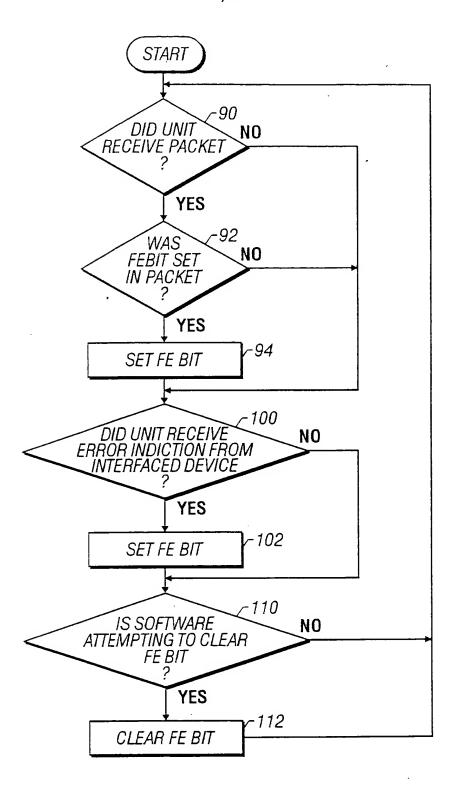
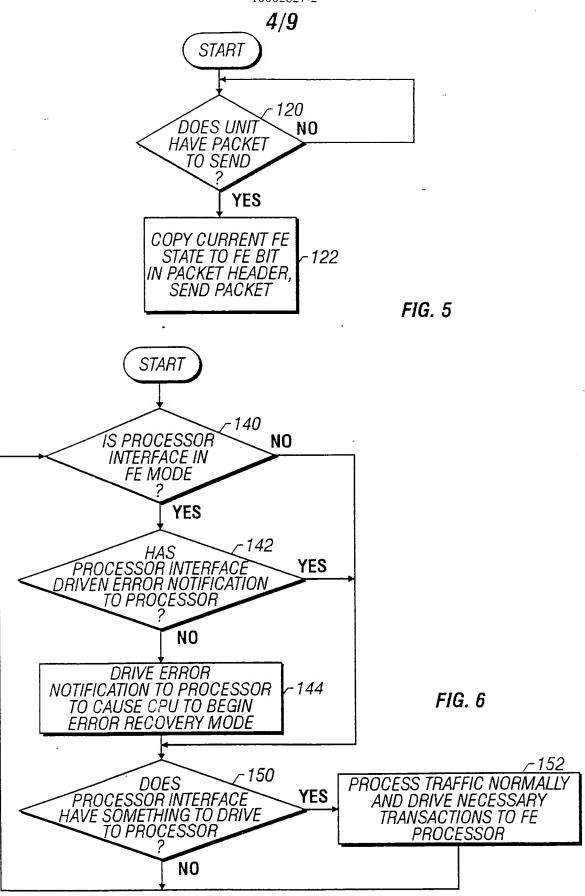


FIG. 4

#### METHOD FOR ALLOWING DISTRIBUTED HIGH PERFORMANCE COHERENT MEMORY WITH FULL ERROR CONTAINMENT

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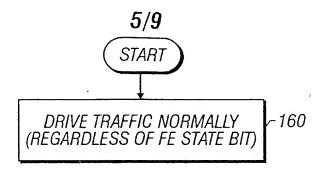
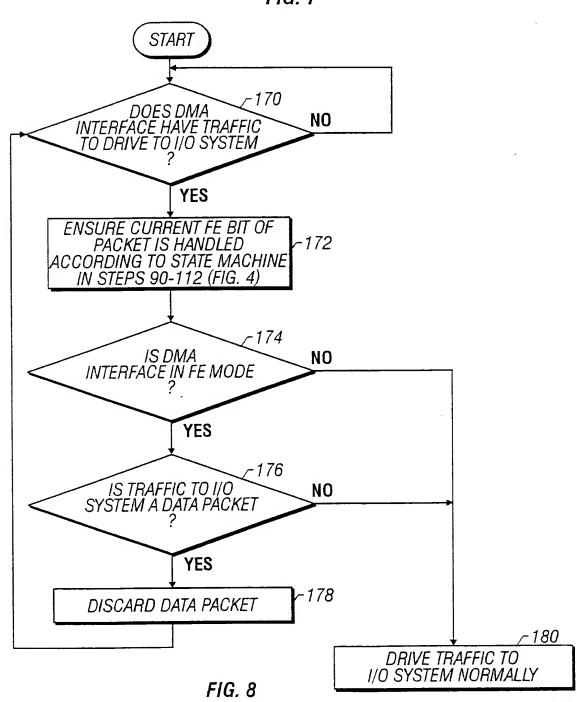
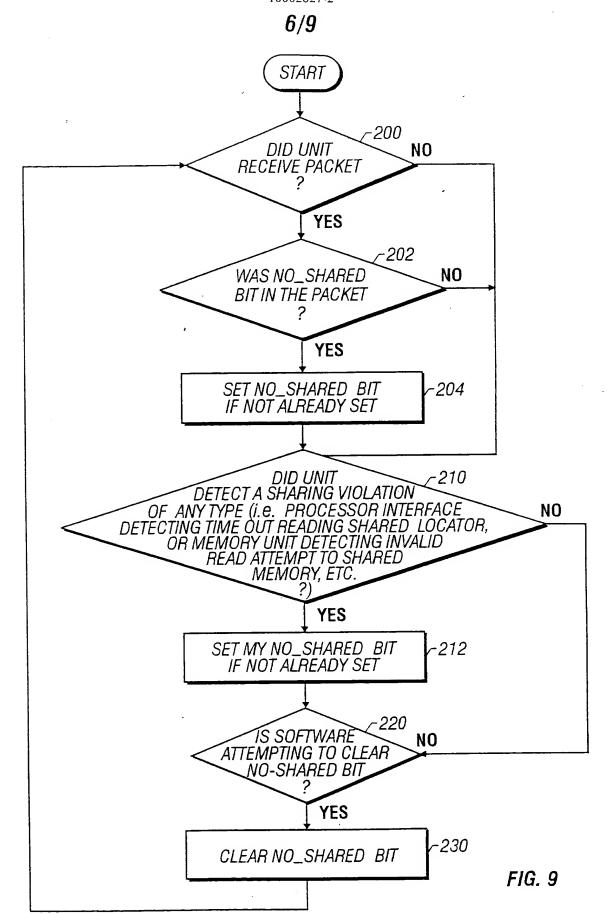


FIG. 7

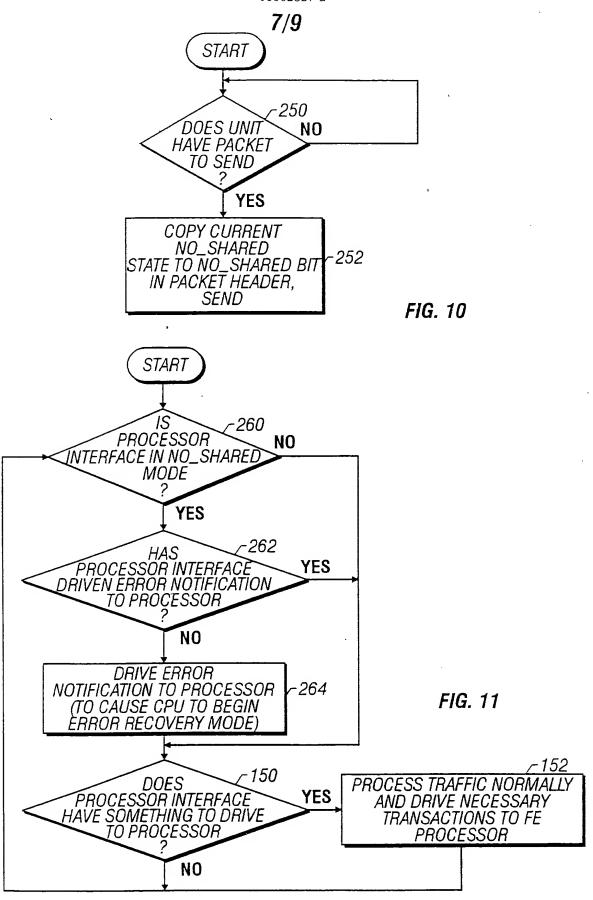


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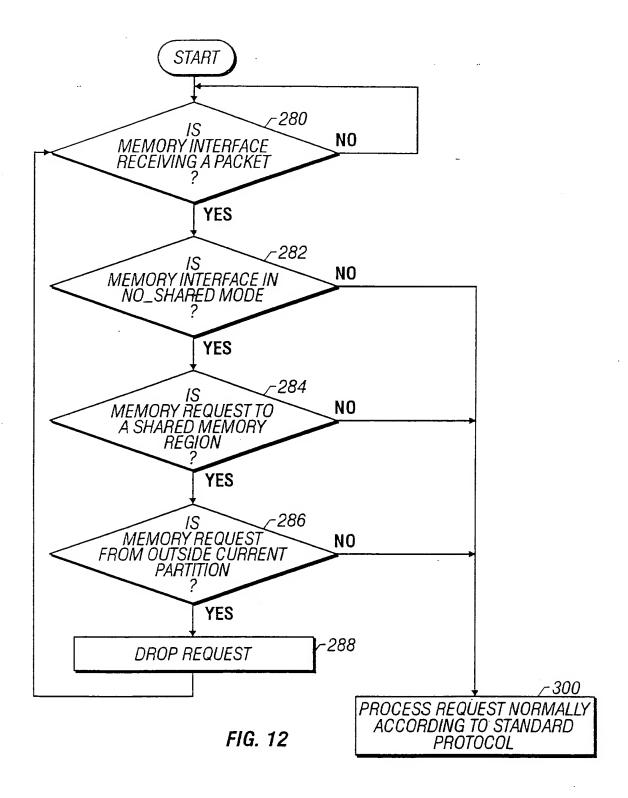


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#### METHOD FOR ALLOWING DISTRIBUTED HIGH PERFORMANCE COHERENT MEMORY WITH FULL ERROR CONTAINMENT

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